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U.S. Serial No.: 10/748,936

REMARKS

The present Amendment is directed to matters of form only and does not affect the substance or patentability of the claims.

The drawings have been amended. FIG. 5 is amended such that NODE3 is transited from the boosting voltage (VPP) to the negative voltage (VNEG) when the decoded row address signal (DRA\_C4) is transited from the ground voltage (VSS) to the supply voltage (VCC), and NODE3 is transited from the negative voltage (VNEG) to the ground voltage (VSS) when the decoded row address voltage (DRA\_C4) is transited from the supply voltage (VCC) to the ground voltage (VSS). The specification has been amended to reflect the changes in FIG. 5 (for further support of the amendments, see the specification as filed at page 7, lines 10-25). The amendments to the drawings and specification correct clerical errors. No new matter is added by the clarifying amendments to the drawings and specification.

Therefore, it is requested that the Amendment be entered and that the application be passed to issue. In the event that a telephone conference will expedite prosecution of the application, the Examiner is invited to telephone the undersigned.

Respectfully submitted,

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**Amendments to the Drawings:**

The attached sheet of drawings includes changes to FIG. 5. The sheet, which includes FIG. 5, replaces the original sheet. FIG. 5 is amended such that NODE3 is transited from the boosting voltage (VPP) to the negative voltage (VNEG) when the decoded row address signal (DRA\_C4) is transited from the ground voltage (VSS) to the supply voltage (VCC), and NODE3 is transited from the negative voltage (VNEG) to the ground voltage (VSS) when the decoded row address voltage (DRA\_C4) is transited from the supply voltage (VCC) to the ground voltage (VSS).

A marked-up version of the drawings, with revisions shown in red, is included with the amended drawings. Entry of the amended drawings is respectfully requested.

Attachment: Replacement Sheet

Annotated Sheet Showing Changes

FIG. 5

